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JC682 U.S. PTO

ATTORNEY'S DOCKET NO.
TI-32942 (032350.B365)

PATENT APPLICATION

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TRANSMITTAL FOR U.S. PATENT APPLICATION
UNDER 37 CFR §1.53(b)

1017 U.S. PTO
10/035606
12/28/01

BOX PATENT APPLICATION
ASSISTANT COMMISSIONER
OF PATENTS
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the original patent application of:

Inventor: James D. Gallia, et al

For: METHOD AND SYSTEM FOR REDUCING CHARGE
DAMAGE IN SILICON-ON-INSULATOR TECHNOLOGY

Enclosed are:

X Specification, Claims and Abstract (24 Pages);

X Drawing(s) 3 Total Sheet(s) of X Formal ___ Informal);

X Combined Declaration and Power of Attorney (3 Pages)
X Newly executed original;

X An Assignment (3 Pages) of the invention to Texas Instruments Incorporated is attached. A separate cover sheet (2 Pages) in compliance with 37 C.F.R. § 3.28 and § 3.31 is included. Please charge the Assignment recordal fee of \$40.00 pursuant to 37 C.F.R. § 1.21(h) to Deposit Account No. 20-0668 of Texas Instruments Incorporated;

X Certificate of Mailing (1 Page);

X TI Return Receipt Postcard (1);

X Baker Botts Return Receipt Postcard (1); and

X This Transmittal (2 pages).

Applicant is:

X Large Entity
___ Small Entity

10035606-122801


The accompanying application is:

X Original.
_____ Continuation _____ Divisional _____ Continuation-In-Part (CIP) of prior application No. _____ which is hereby incorporated by reference therein.

FEE CALCULATION					FEE
	Number	Number Allowed	Number Extra	Rate	Basic Fee \$ 740.00
Total Claims	20	20	0	X \$18.00 =	\$ 0.00
Independent Claims	3	3	0	X \$84.00 =	
TOTAL FILING FEE =					\$740.00

X Please charge the filing fee of \$740.00 and any other fee required by this paper, or credit any overpayment, to Deposit Account No. 20-0668 of TEXAS INSTRUMENTS INCORPORATED. **This document is being transmitted in duplicate.**

BAKER BOTTS L.L.P.
Attorneys for Applicants



Bradley P. Williams
Registration No 40,227

Date: December 28, 2001

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: GALLIA, et al
Filing date: December 28, 2001
Title: *Method and System for Reducing Charge
Damage in Silicon-on-Insulator Technology*

BOX PATENT APPLICATION

Honorable Assistant Commissioner

For Patents

Washington, D.C. 20231

Dear Sir:

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that the attached Patent Application, formal drawings, Declaration and Power of Attorney, Assignment, transmittal forms, return postcard for Baker Botts L.L.P., return postcard for Texas Instruments Incorporated, and this Certificate of Mailing are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on this 28th day of December, 2001, and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.


Willie Jiles

Express Mail Receipt
No. EL 759192980 US
Attorney's Docket:
032350.B365 (TI 32942)